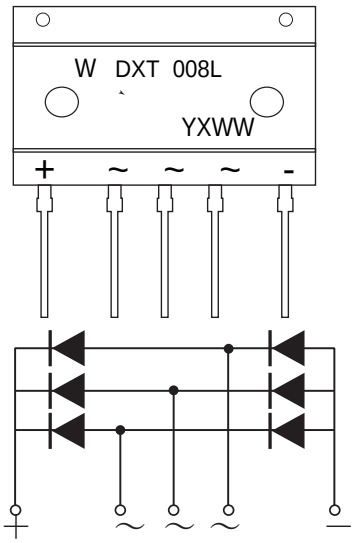
# DXT6008L

23-NOV-2021

## Low VF Three-phase Bridge Rectifiers




### )HDWXUHV

‡*ODVV 3DVVLYDWHG &KLS -XQFW
‡/RZ ,550
‡/RZ 9)
‡+LJK 9550
‡6SHFLDO IUDPH GHVLJQ IRU KHD

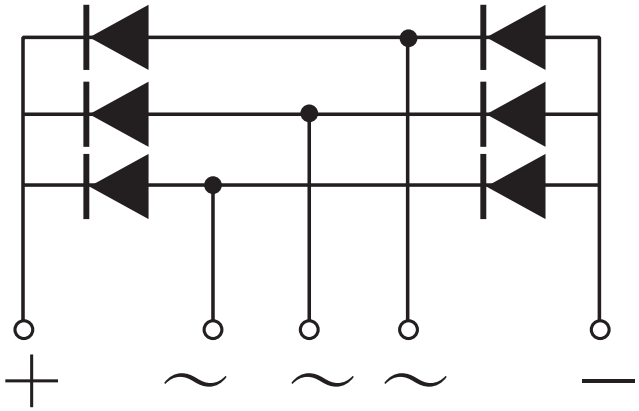
### %HQHILWV

- Case: DXT
- Terminals: Solderable Per MIL-STD-750
- Reduced power loss and switching transistor

| 3DUDPHWHU | Symbols | ';**T60** / | 8QLWV |
|---|---|---|---|
| Maximum Repetitive Peak Reverse Voltage | $V_{RRM}$ | 800 | V |
| Maximum RMS voltage | $V_{RMS}$ | 560 | V |
| Maximum DC Blocking Voltage | $V_{DC}$ | 800 | V |
| Average Rectified Output Current | $I_o$ | 60 | A |
| Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method) | $I_{FSM}$ | 450 | A |
| $I^2t$ rating for fusing ( 1ms< t < 10ms) | $I^2t$ | 840 | $A^2S$ |
| Type Forward Voltage at 30.0A | $V_F$ | 0.93 | V |
| Maximum Forward Voltage at 30.0 A | | | |
| Maximum DC Reverse Current @TA=25 °C<br>at Rated DC Blocking Voltage @TA=125 °C | $I_R$ | 10<br>500 | μA |
| Typical Junction Capacitance Note1 | $C_j$ | 50 | pF |
| Operating and Storage Temperature Range | $T_j$, $T_{stg}$ | -55 ~ +1 | °C |

Note: 1. Measured at 1MHz and applied reverse voltage of 4 VDC.
2. Mounted on glass epoxy PC board with 4×1.5"×1.5"（3.81×3.81 cm）copper pad.

## RATINGS AND CHARACTERISTICS CURVES (TA = 25 ˚C unless otherwise noted)

**Figure 1. Derating Curve**

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